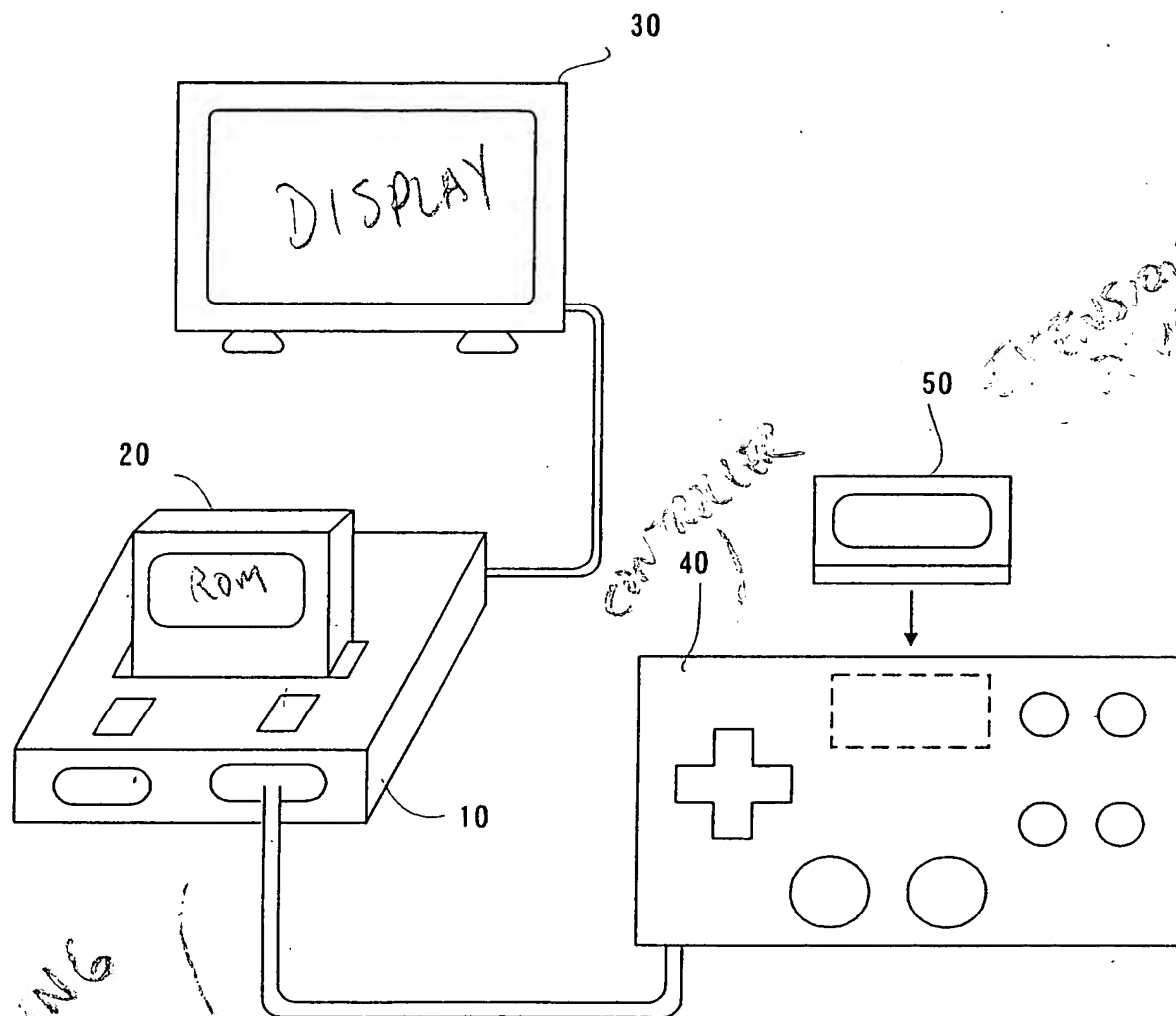


APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 1



65070-08E2250

controller

display

DRIVING
FOR
DATA

FIG. 2

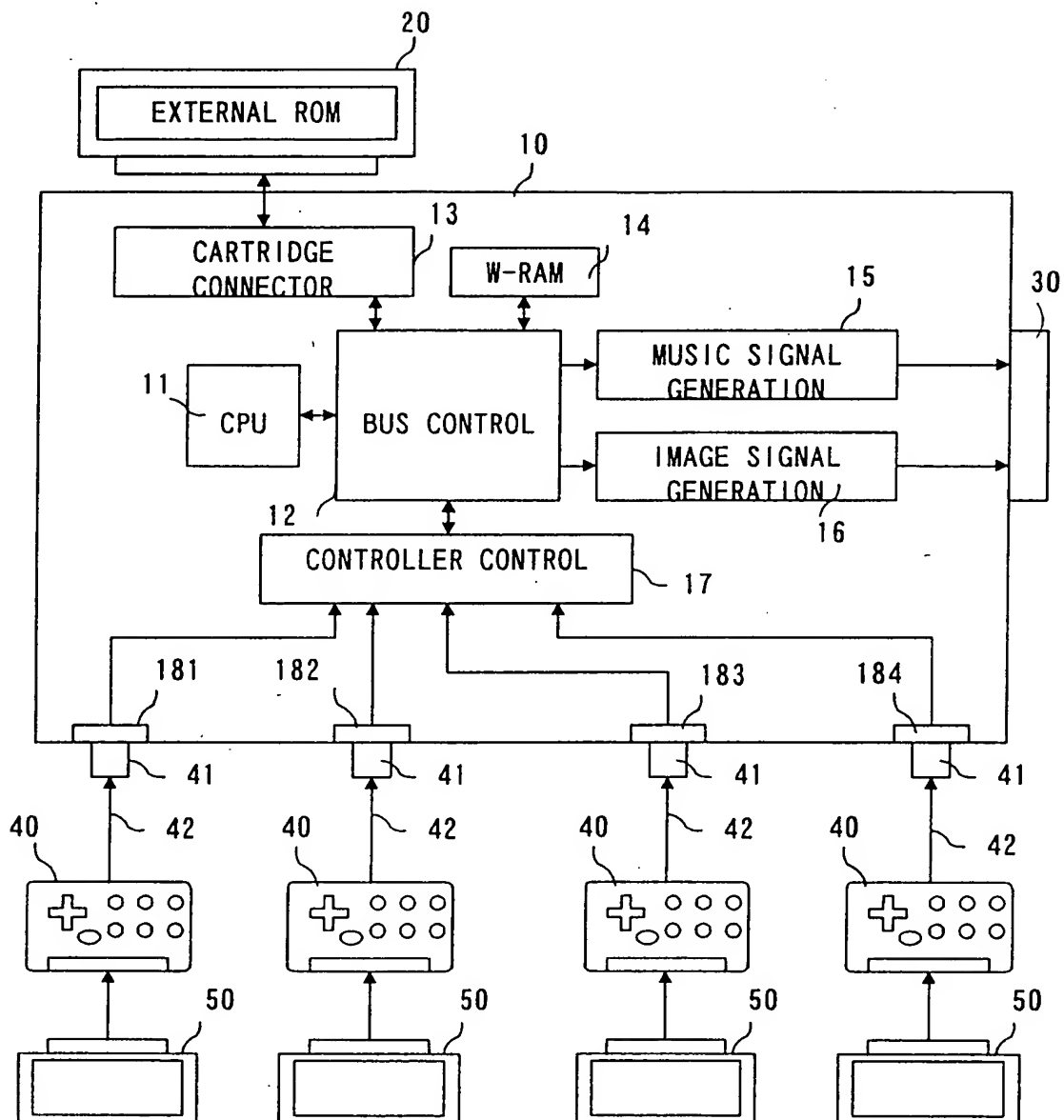
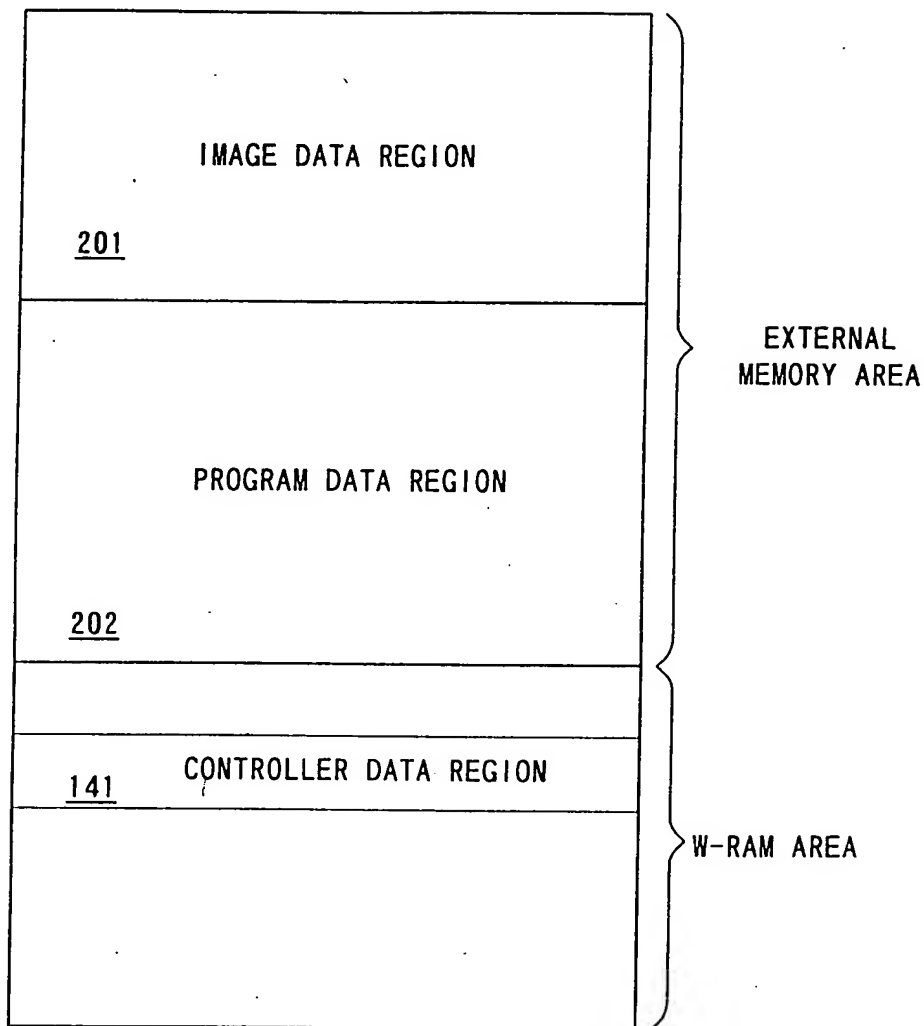


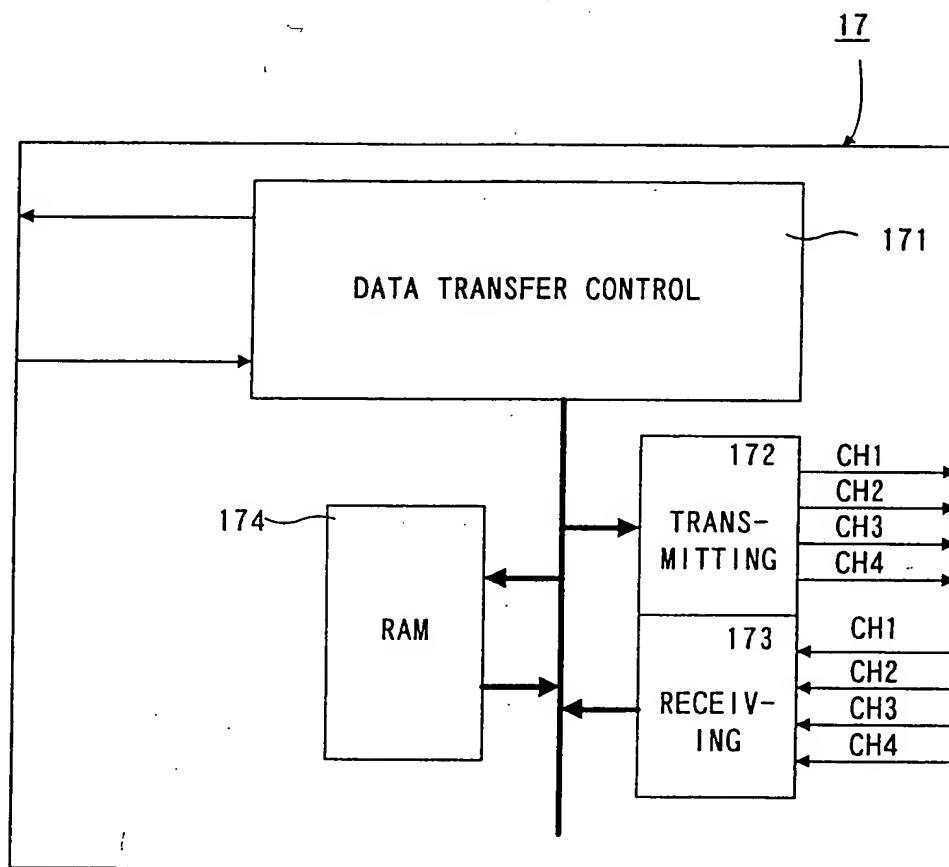
FIG. 3

CPU MEMORY MAP



66070-082250

FIG. 4



660070-052266

FIG. 7

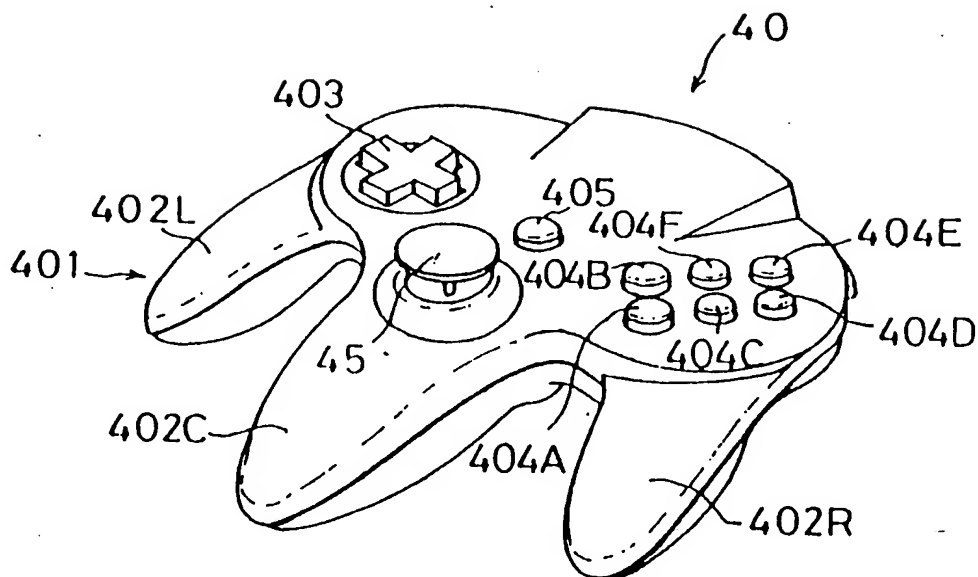


FIG. 8

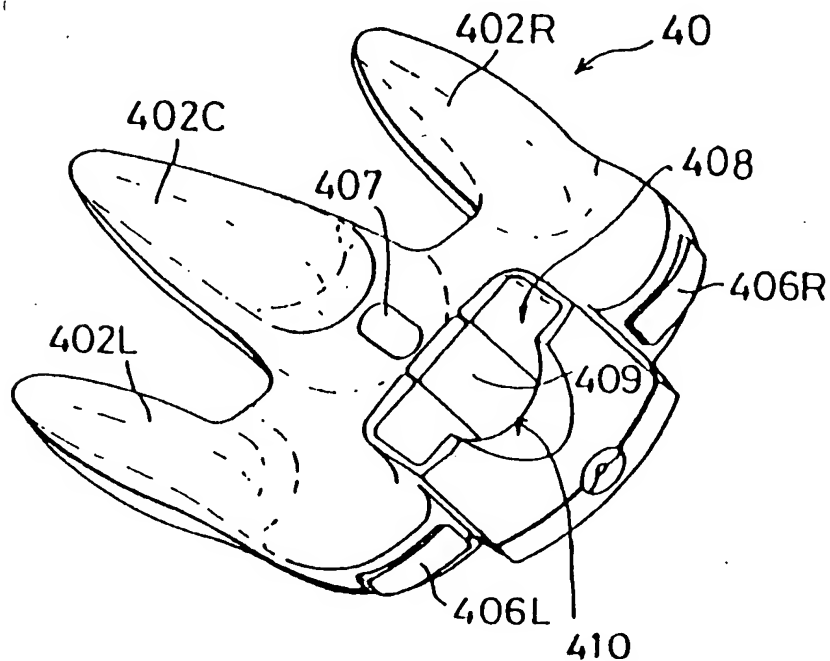
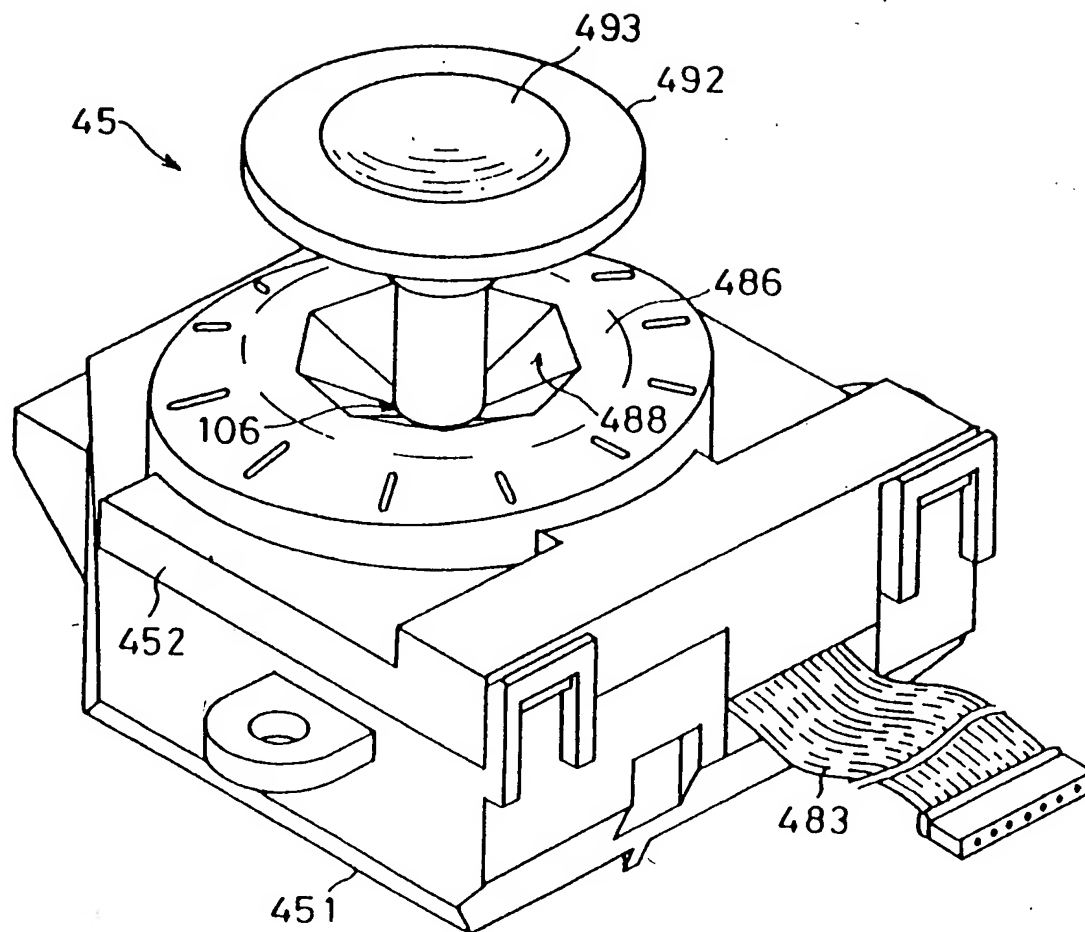
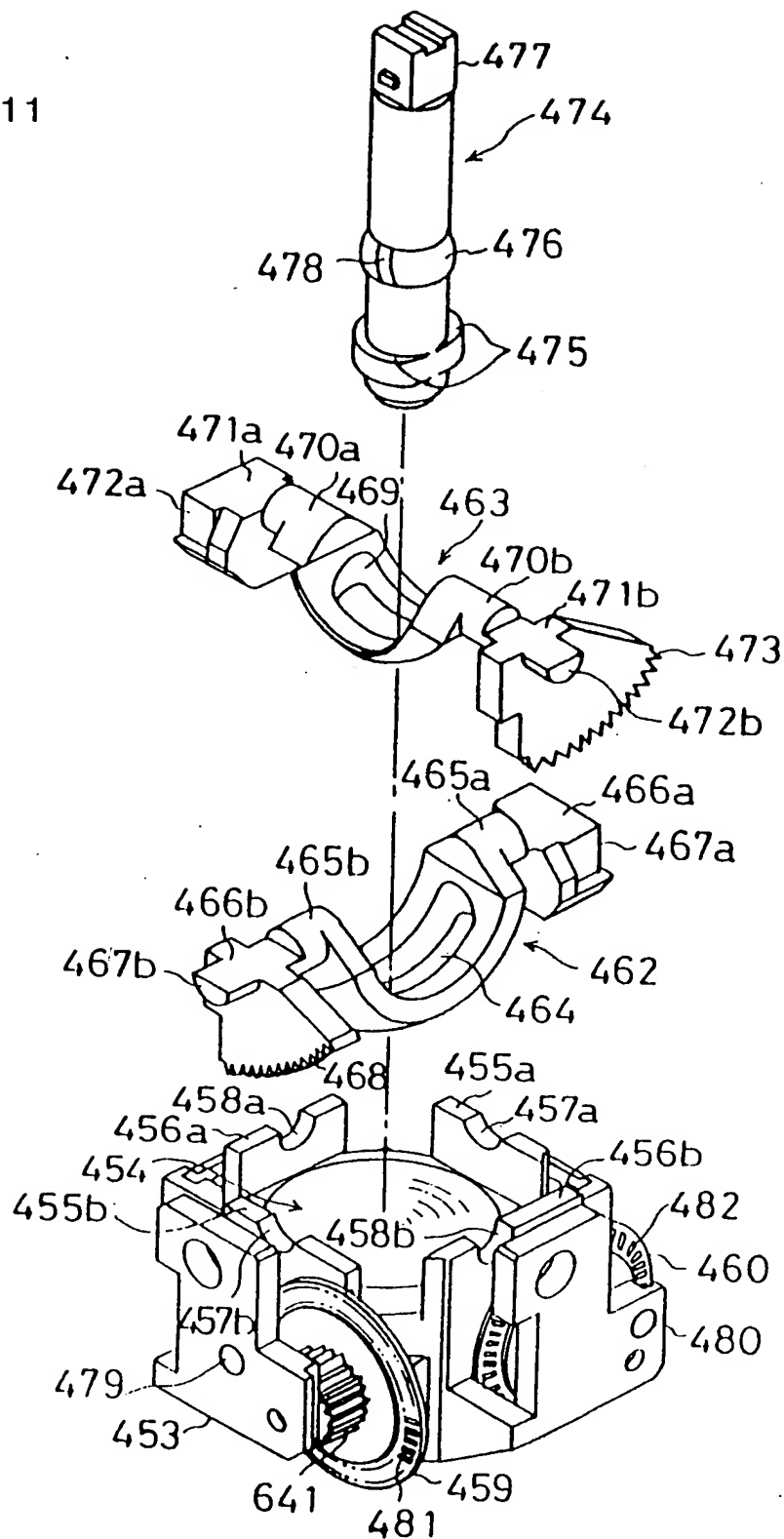


FIG. 9



683010-0522260

FIG. 11



668070-05242280

FIG. 12

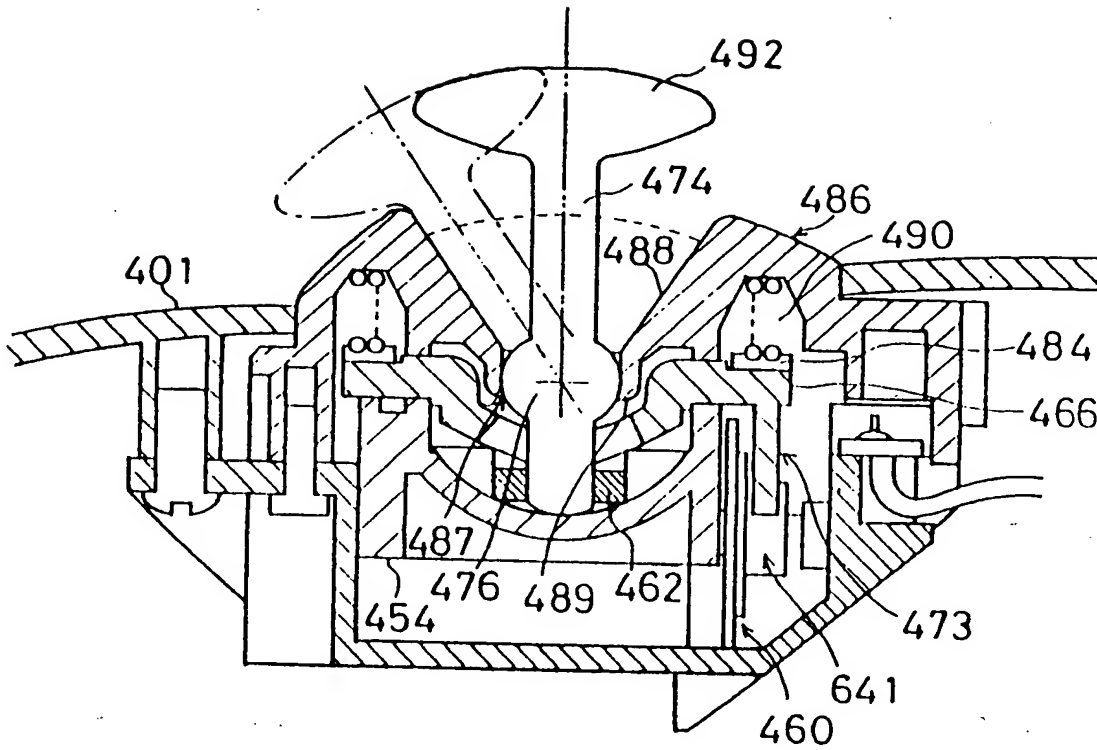


FIG. 13

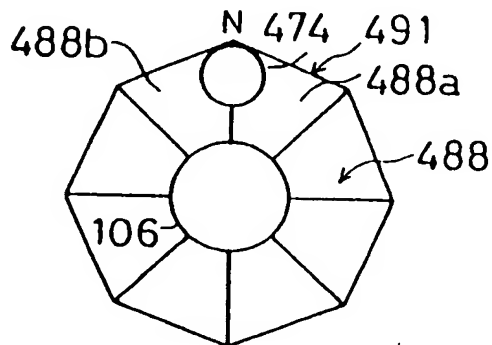
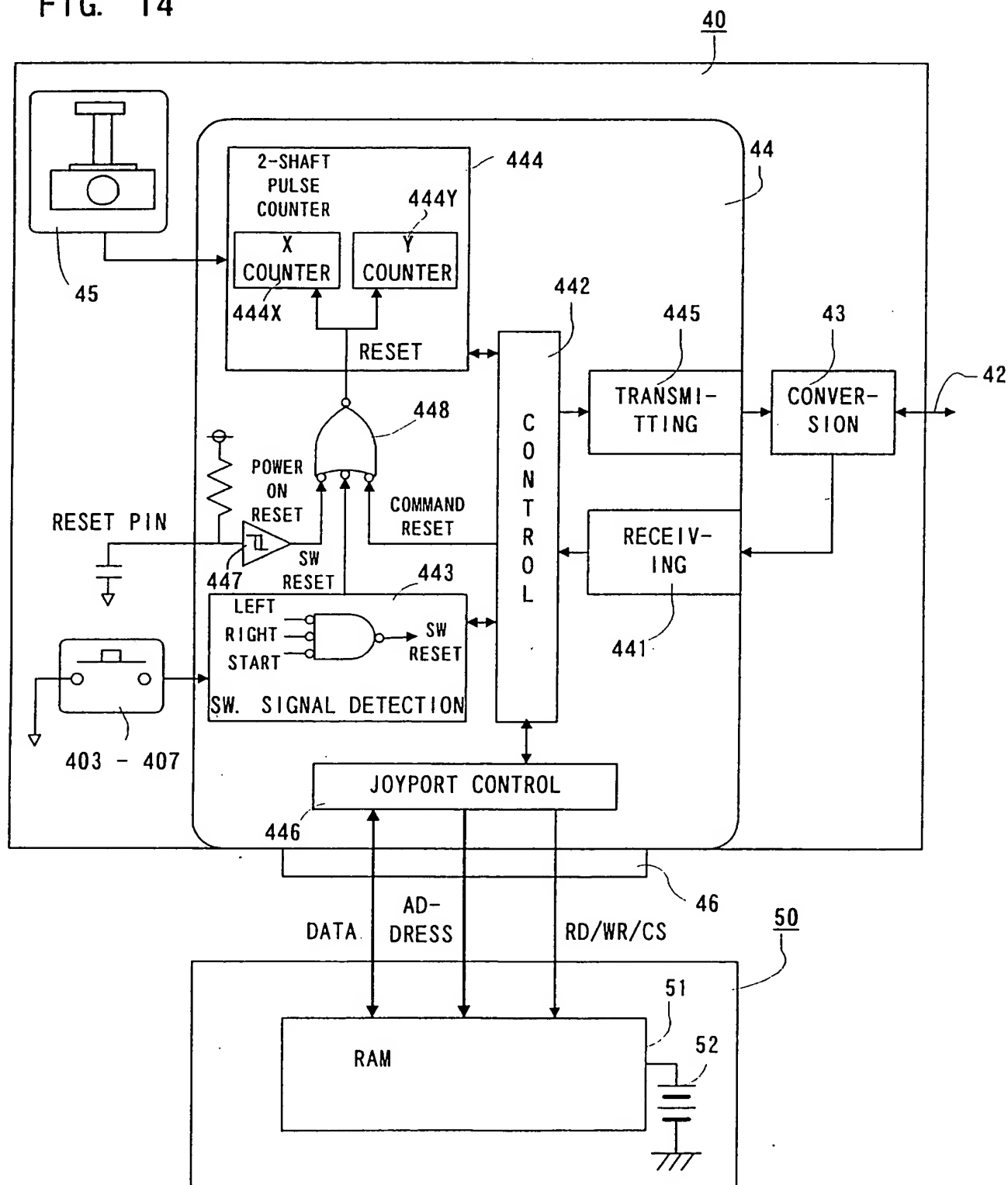


FIG. 14



APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

FIG. 15

1 BYTE	B	A	G	START	↑	↓	←	→
2 BYTE	JSRST	0	L	R	E	D	C	F
3 BYTE	<div> <div>X</div> <div>ORDINATE</div> </div>							
4 BYTE	<div> <div>Y</div> <div>ORDINATE</div> </div>							

662070 0022360

FIG. 16

COMMAND 0: TRANSMITTING TYPE OF CONTROLLER
 RECEPTION: 1 BYTE TRANSMISSION: 3 BYTES

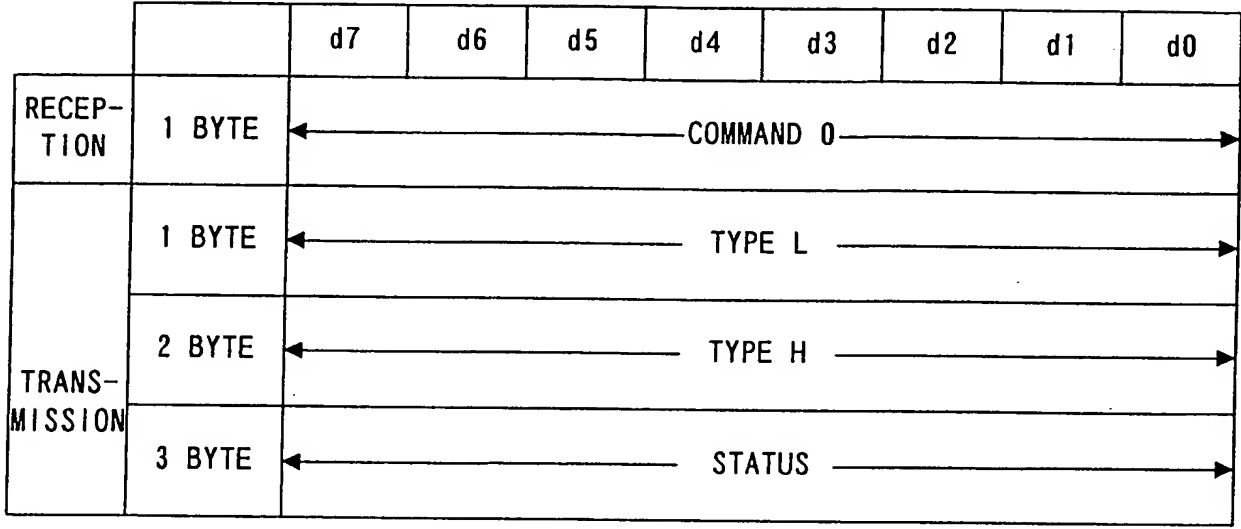
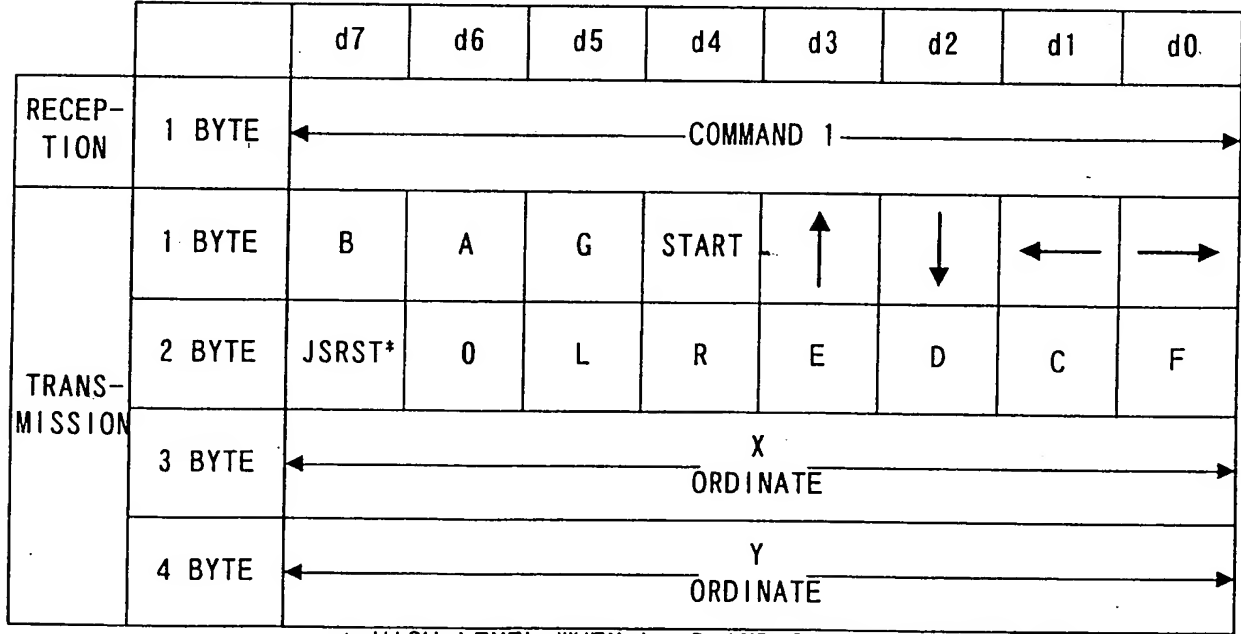


FIG. 17

COMMAND 1: ACCESSING STANDARD CONTROLLER
 RECEPTION: 1 BYTE TRANSMISSION: 4 BYTES



* HIGH LEVEL WHEN L, R AND START BUTTONS ARE
 SIMULTANEOUSLY DEPRESSED

66070 000000

FIG. 18

COMMAND 2: READ-OUT RAM
RECEPTION: 3 BYTES TRANSMISSION: 33 BYTES

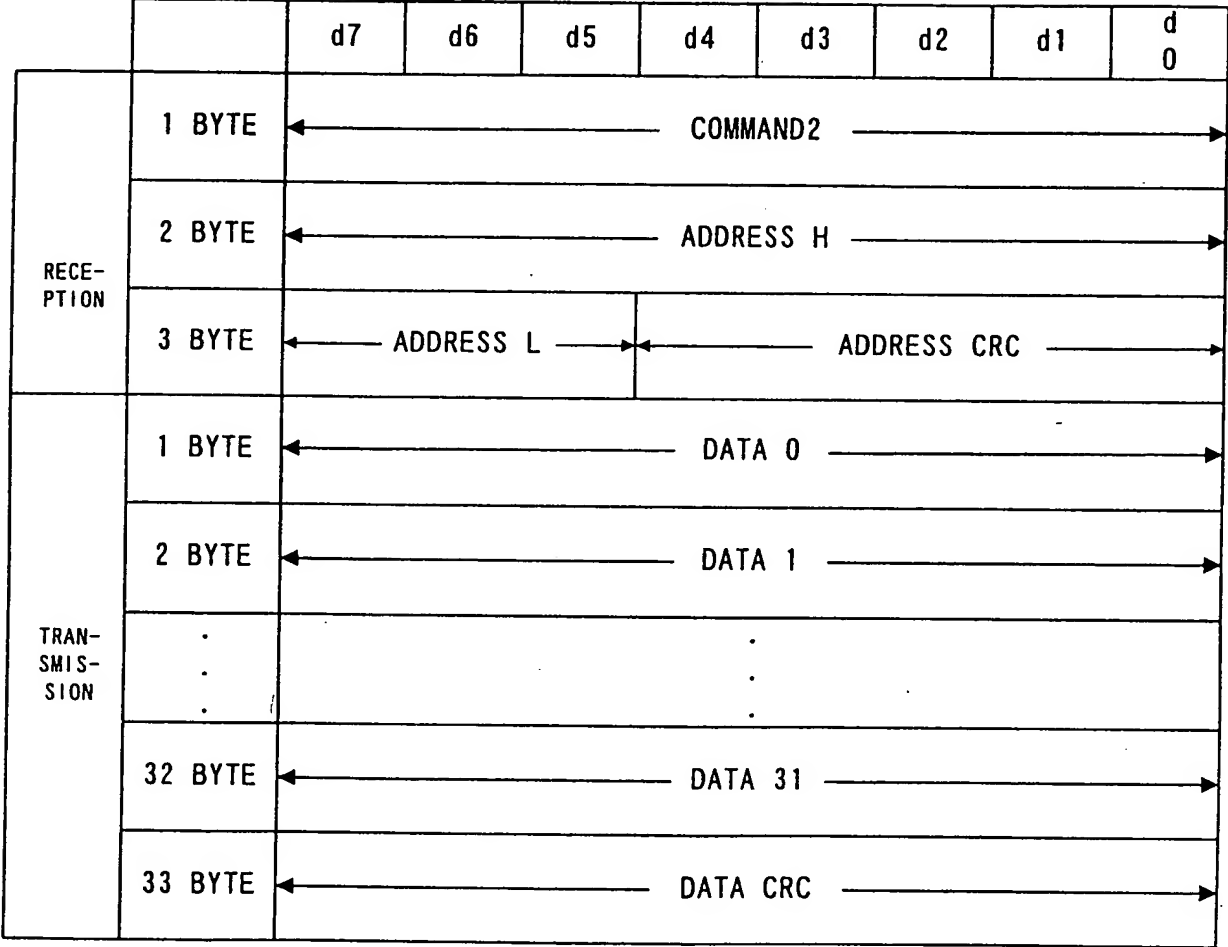
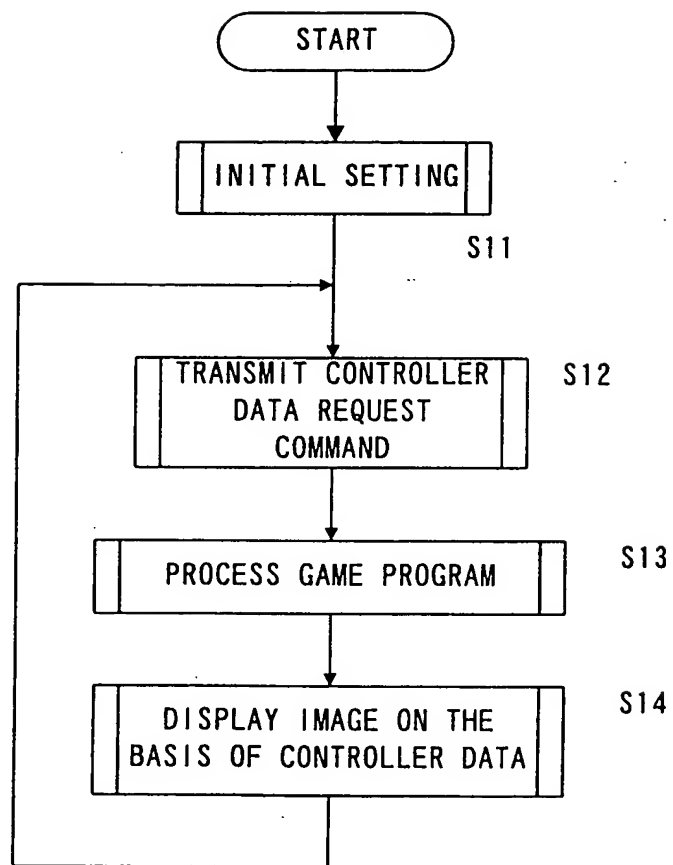
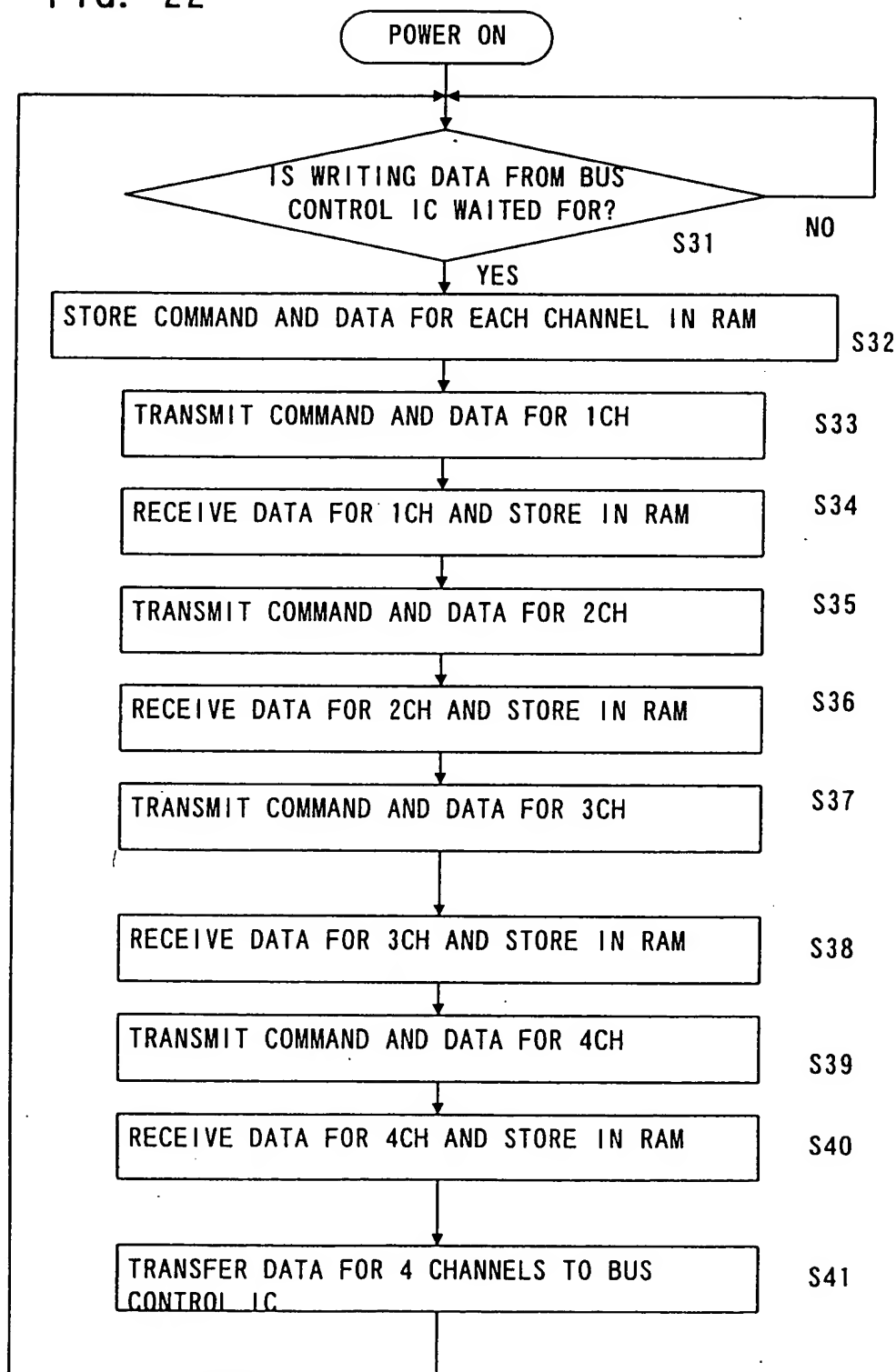


FIG. 20



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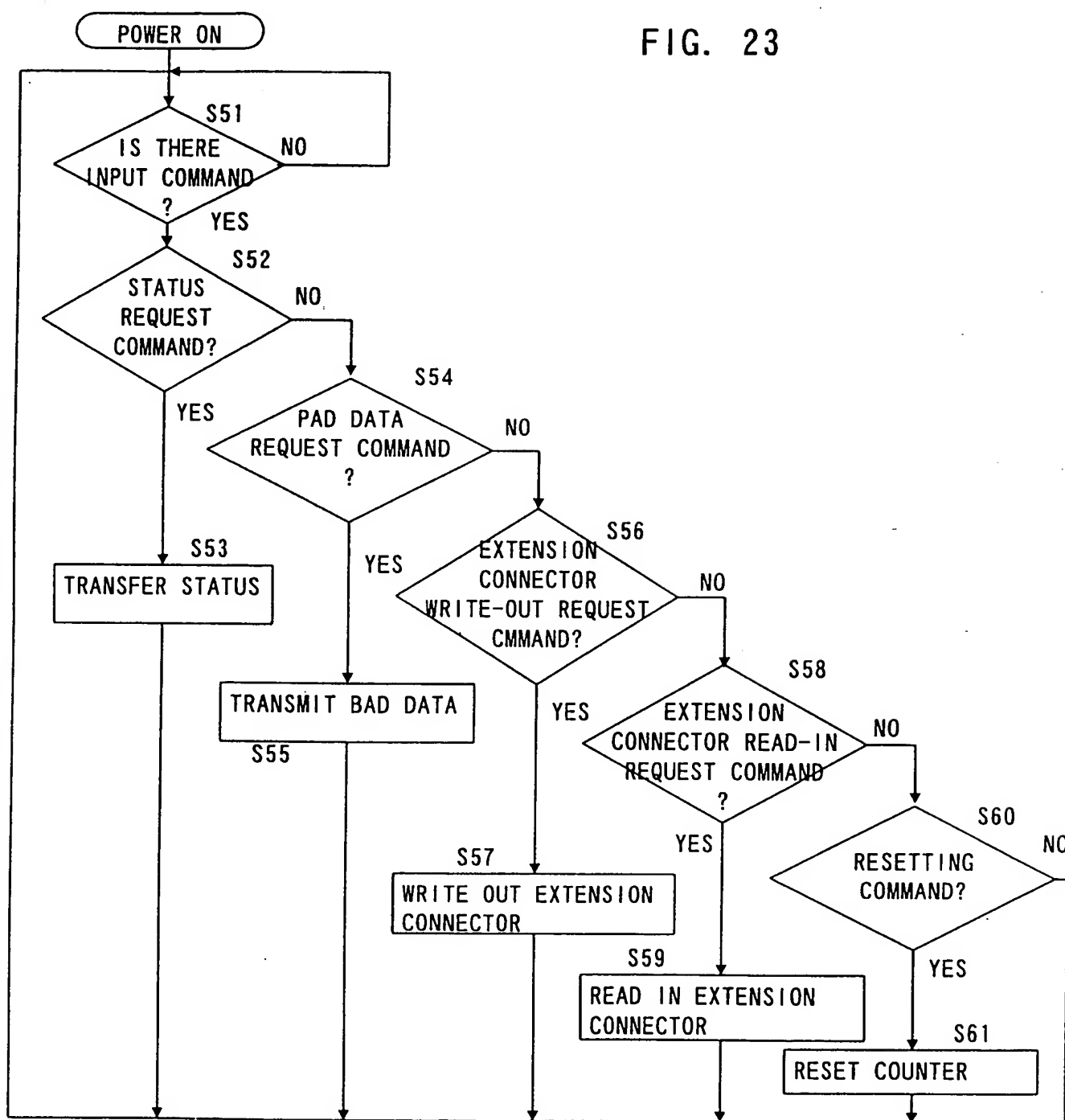


FIG. 24

FIG. 25

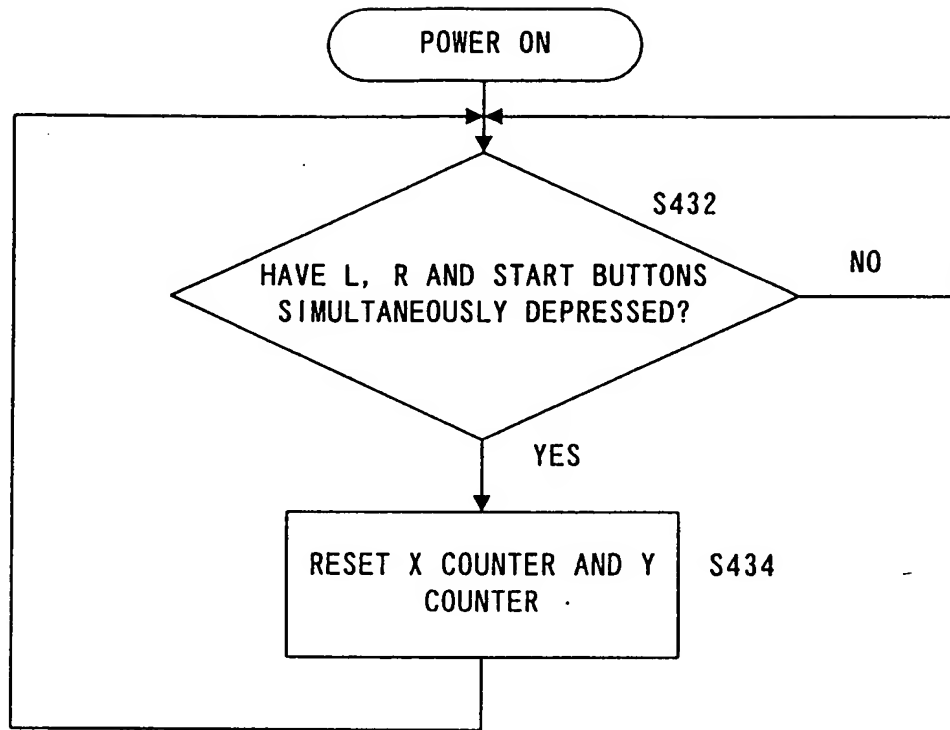
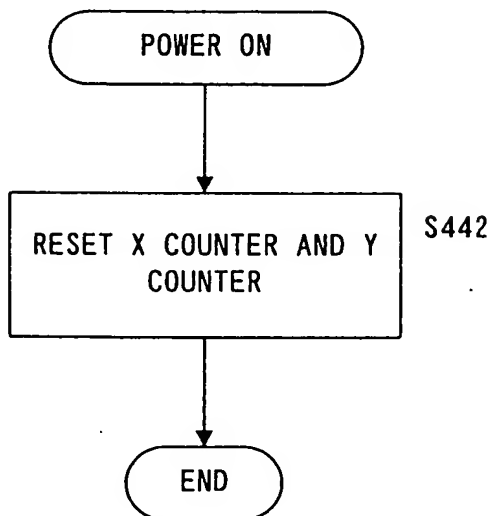
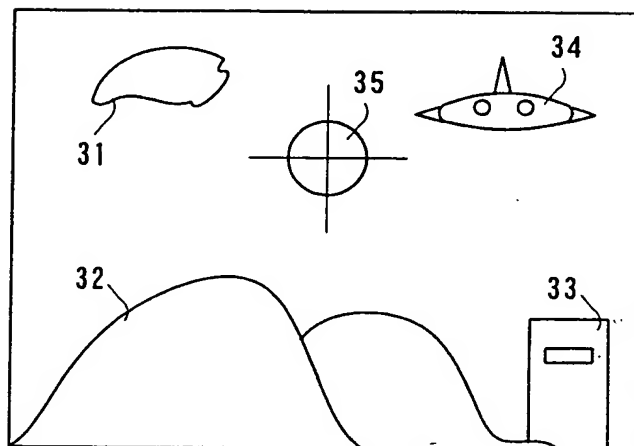


FIG. 26



PHYSICAL COORDINATE OF JOYSTICK



PHYSICAL COORDINATE OF JOYSTICK

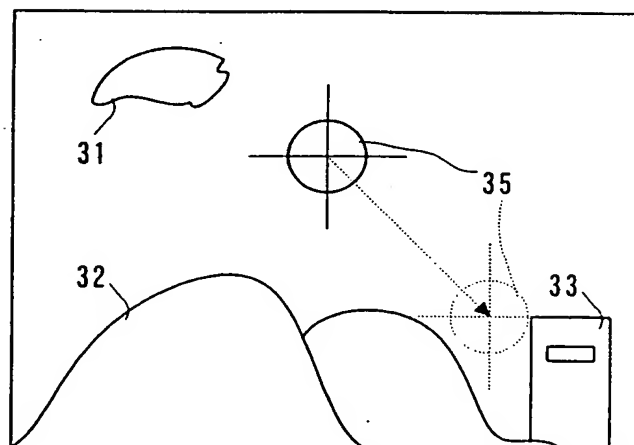
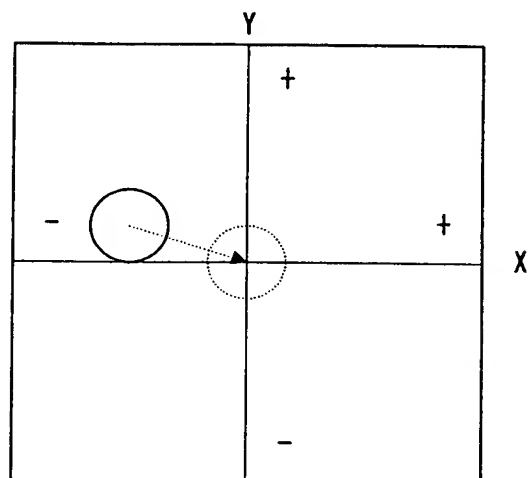


FIG. 29

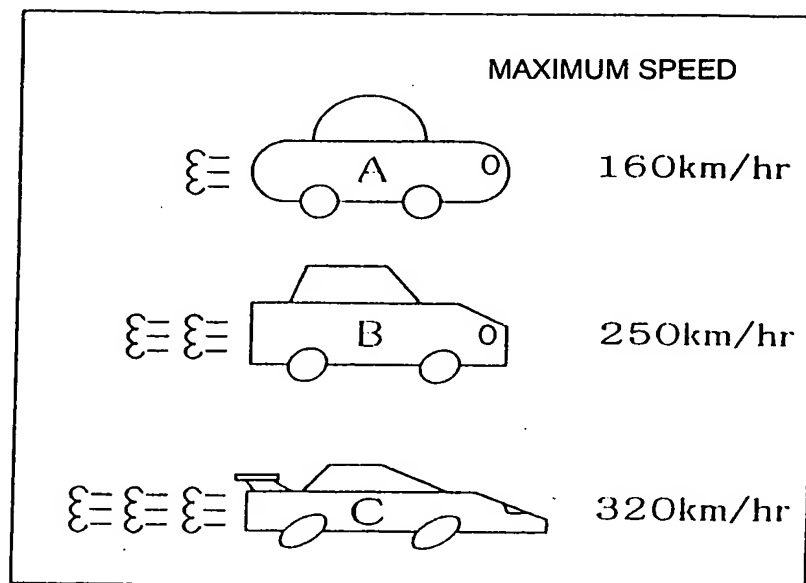
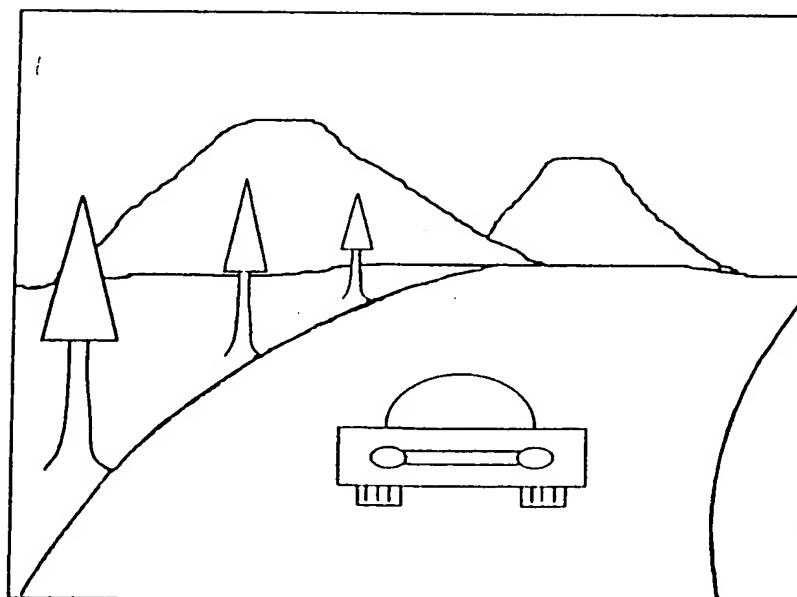
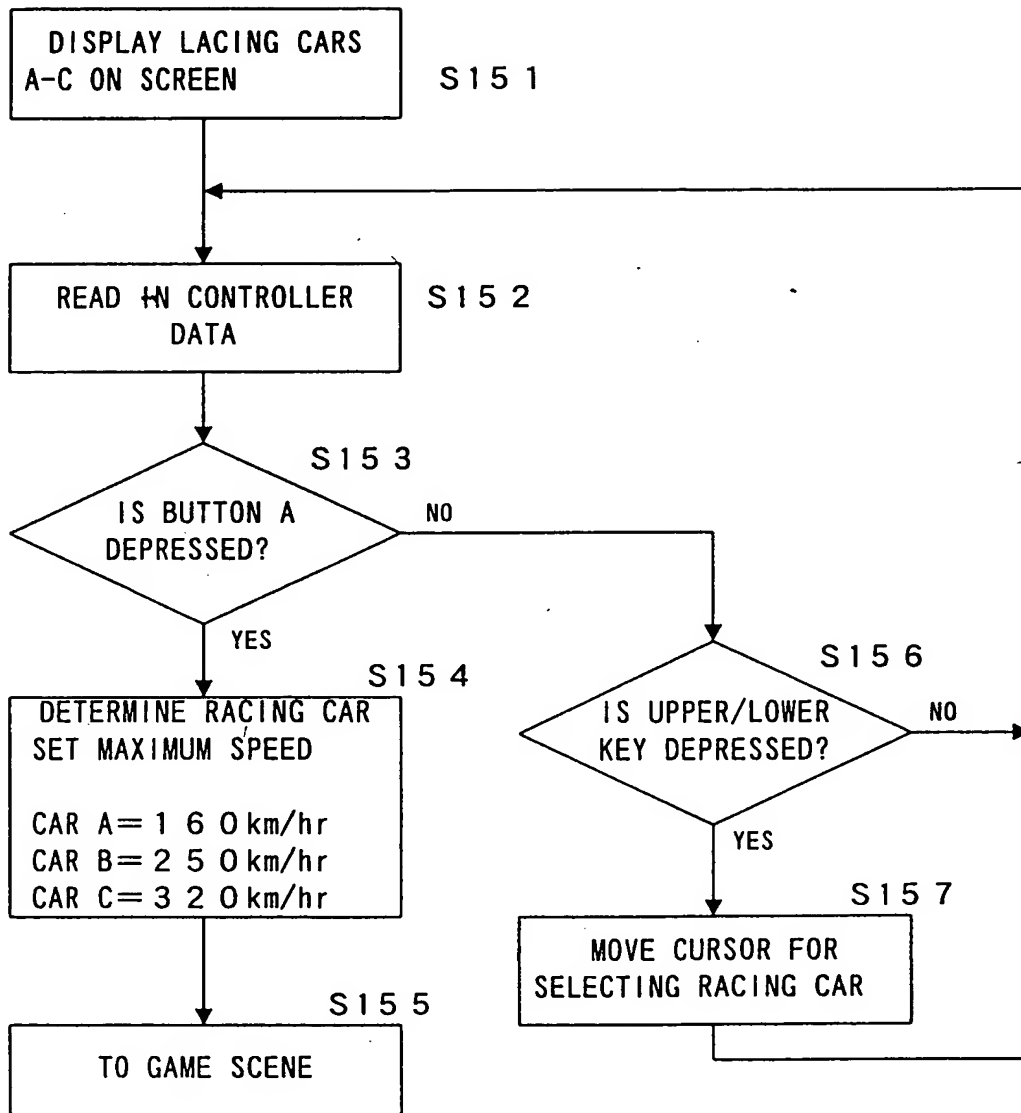


FIG. 30



668070-0342260

FIG. 31



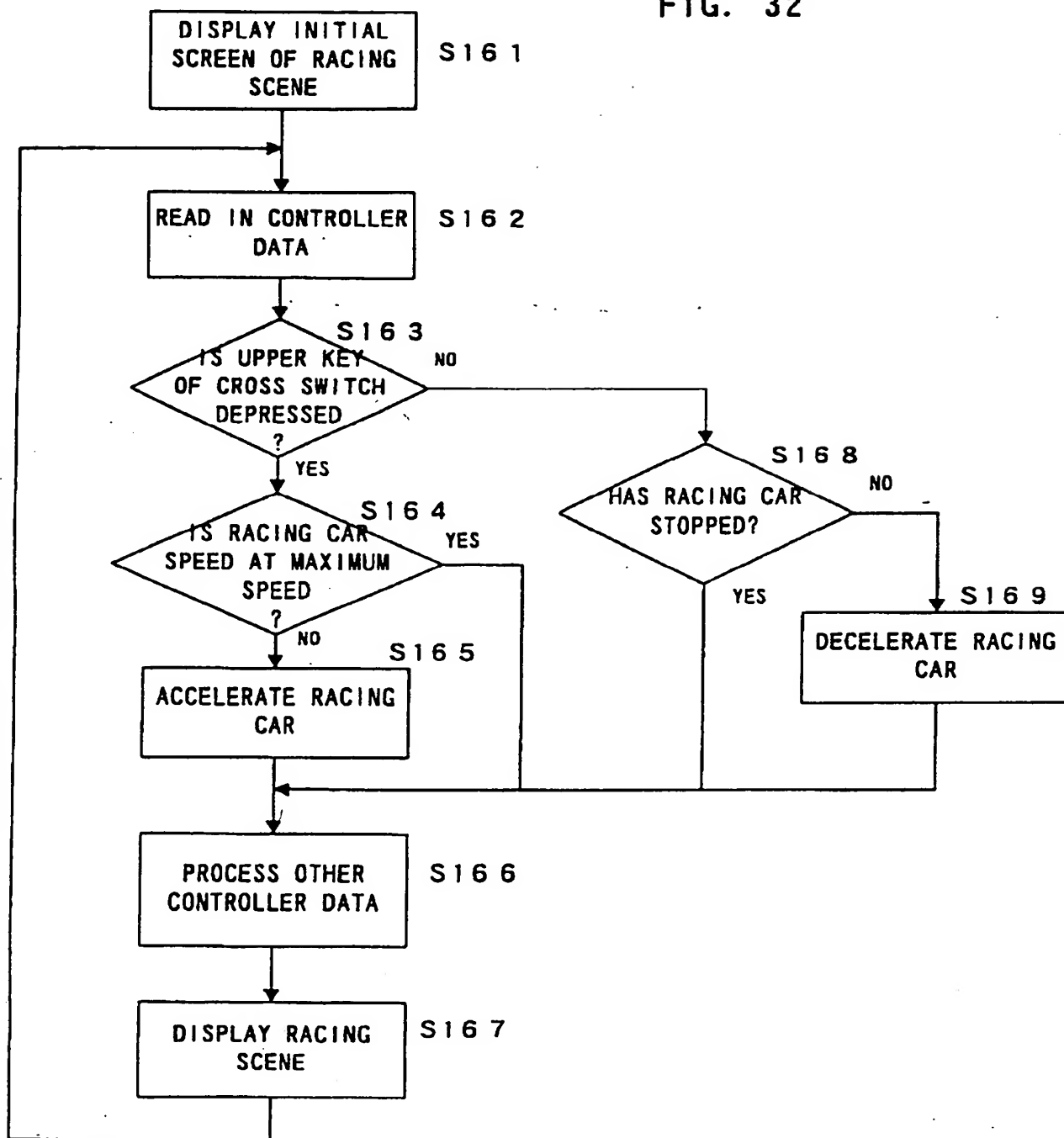


FIG. 34